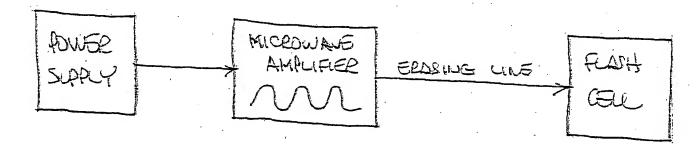
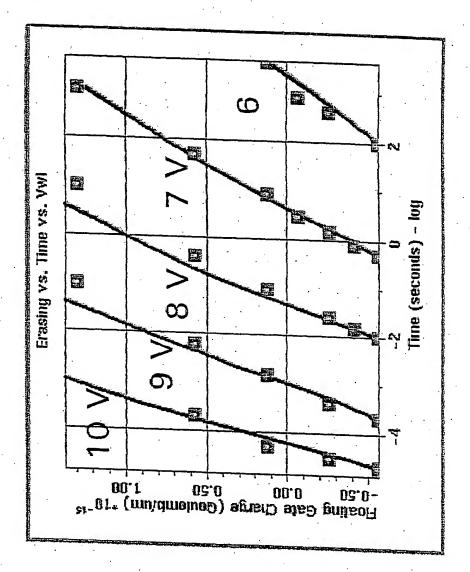


F16. 2B

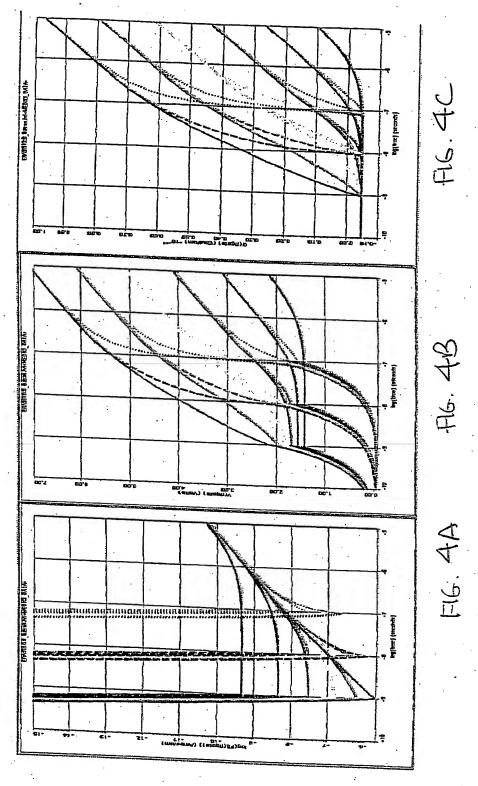


F16.2C



FG charge VS. Erasing time VS. Erasing voltage (squares – experimental, solid lines – simulated)

F16.3



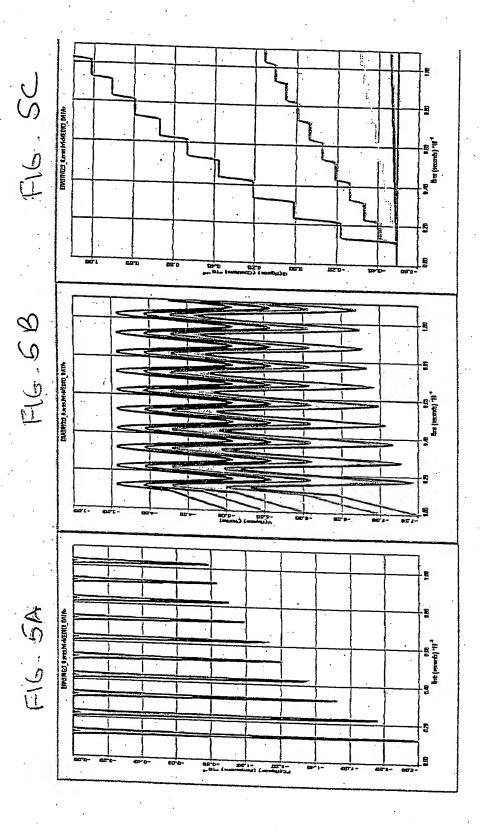
. Pulse method.

a) Erasing current VS. Time VS. Erasing voltage

VS. Time VS. Erasing voltage

VS. Time VS. Erasing voltage b) FG voltagec) FG charge

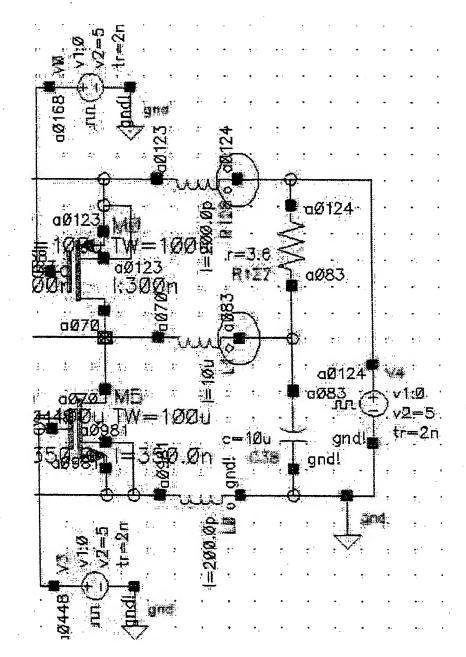
solid - 1 ns, dashed -10 ns, dotted 100 ns , 14V, 15V 10V, 11V, 12V,



d) FG voltage VS, Time VS. Erasing voltage c) FG charg VS. Time VS. Erasing voltag 10V, 11V, 12V, , 14V, 15V

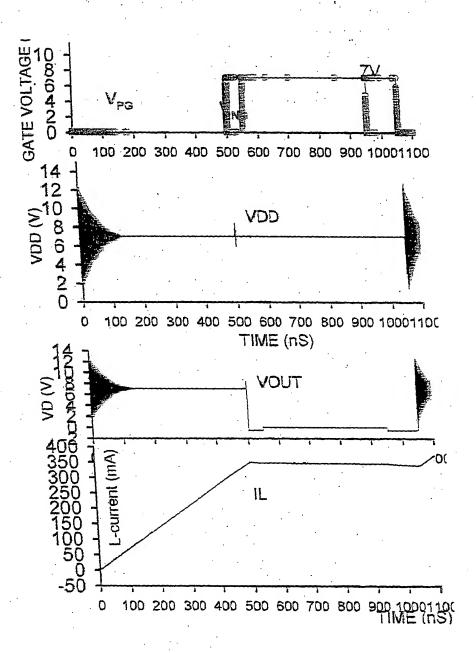
c) Erasing current VS. Time VS. Erasing voltage

RF method.



Example of the circuit for pulse generation

F16.6



F16.7